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# PARTIAL RECONFIGURABLE FIR FILTERS USING DPR



LAP Lambert Academic Publishing Dez 2014, 2014. Taschenbuch. Book Condition: Neu. 220x150x6 mm. This item is printed on demand - Print on Demand Neuware - Reconfigurable hardware might be the next step which will give computer performance a new big leap forward. The idea is to use the, nowadays, high performance FPGA technology to adapt the hardware to the problem. Dynamic partial reconfiguration of FPGA offers new design space with variety of benefits. This Dissertation intends to describe the development...

### Read PDF Partial Reconfigurable FIR Filters using DPR

- Authored by Prudhvi Sai Rangiseti
- Released at 2014



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